Docket No.

251822US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Noboru MATSUDA, et al.

SERIAL NO: New Application

GAU:

FILED:

Herewith

EXAMINER:

FOR:

SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Marvin J. Spivak

Registration No. 24,913

C. Irvin McClelland Registration Number 21,124

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THE SAME

STATEMENT OF RELEVANCY

Reference AO (7-50411) of Form PTO-1449:

A gate insulation film is minded on a silicon board. The 1st polycrystal silicon film, the gate electrode of the multilayer structure which consists of the 1st TiSi₂ film and the 2nd polycrystal silicon film is formed. After forming a source and a drain by using a gate electrode as a mask, the 2nd titanium film which formed the 2nd titanium film in the whole surface, and was formed on the gate electrode, the source, and the drain is TiSi₂ and the 2nd TiSi₂ film is formed.

Reference AP (2001-345446) of Form PTO-1449:

This reference discloses a trench gate type MOSFET in which a gate electrode has a silicide layer.

Form PTO 1449	U.S. DEPARTMENT OF COMMERCE			ATTY DOCKET NO.		SERIAL NO.		
(Modified)		PATENT AND TRAD	DEMARK OFFICE	251822US2	New Application			
				APPLICANT				
LIST OF I	REFER	RENCES CITED BY AP	PLICANT	Noboru MATSUDA, et al.				
				FILING DATE		GROUP		
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U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB FILING DATE CLASS IF APPROPRIATE		
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FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY		TRANS YES		LATION NO
	AO	7-50411	02/21/95	Japan (with English abstract)				X
	AP	2001-345446	12/14/01	Japan (with English abstract)				X
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)								
	AW							
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Examiner					Date Considered			
*Examiner: In conformance	itial if r	eference is considered transidered. Include of	, whether or no copy of this form	t citation is in conformance with MPEP 60 n with next communication to applicant.	09; Draw li	ne through	citation	if not in